

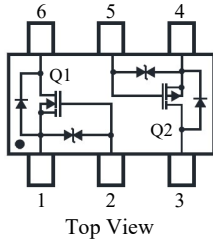


# SMX06C7KDTH

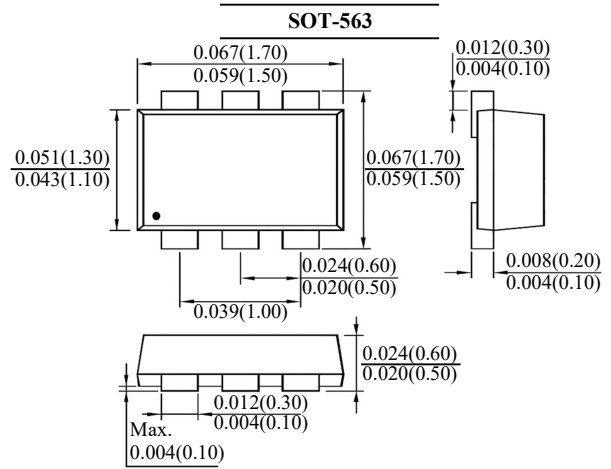
## Complementary Pair Enhancement Mode MOSFET

### FEATURES

- Low on resistance  $R_{DS(ON)}$
- Low gate threshold voltage
- ESD protected
- Suffix "H" indicates Halogen-free parts, ex. SMX06C7KDTH



1.Source 2.Gate 3.Drain  
4.Source 5.Gate 6.Drain



### Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Q1 Value	Q2 Value	Unit
Drain-Source Voltage	$V_{DS}$	60	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	$\pm 20$	V
Continuous Drain Current	$I_D$	0.57	-0.35	A
Peak Drain Current (Note 1)	$I_{DM}$	-1.2	-1.0	A
Power Dissipation (Note 2)	$P_D$	500		mW
Thermal Resistance from Junction to Ambient (Note 2)	$R_{\theta JA}$	250		$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	$T_J, T_{stg}$	- 55 to + 150		$^\circ\text{C}$

Note:

1. Pulse Test: Pulse Width  $\leq 100\mu\text{s}$ , Duty Cycle  $\leq 2\%$ , Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^\circ\text{C}$ .
2. Device mounted on FR-4 substrate PC board, 2oz copper with 1-inch<sup>2</sup> copper plate in still air.



# SMX06C7KDTH

## Complementary Pair Enhancement Mode MOSFET

### Electrical Characteristics ( $T_A = 25\text{ }^\circ\text{C}$ unless otherwise specified)

#### N-Channel Q1

Parameter	Conditions	Symbol	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain Source Breakdown Voltage	$I_D = 250\mu\text{A}$	$V_{(BR)DSS}$	60	-	-	V
Zero Gate Voltage Drain Current	$V_{DS} = 48\text{V}$	$I_{DSS}$	-	-	1	$\mu\text{A}$
Gate Source Leakage Current	$V_{GS} = \pm 20\text{V}$	$I_{GSS}$	-	-	$\pm 10$	$\mu\text{A}$
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	$V_{GS(th)}$	0.8	-	1.5	V
Static Drain Source On-Resistance	$V_{GS} = 10\text{V}, I_D = 0.5\text{A}$	$R_{DS(on)}$	-	-	1.6	$\Omega$
	$V_{GS} = 4.5\text{V}, I_D = 0.2\text{A}$		-	-	2.5	
	$V_{GS} = 2.5\text{V}, I_D = 0.1\text{A}$		-	-	4.5	
<b>Dynamic</b>						
Total Gate Charge	$V_{DS} = 25\text{V}, V_{GS} = 4.5\text{V}, I_D = 1\text{A}$	$Q_g$	-	0.9	-	nC
Gate-Source Charge		$Q_{gs}$	-	0.5	-	
Gate-Drain Charge		$Q_{gd}$	-	0.3	-	
Input Capacitance	$V_{DS} = 25\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$	$C_{iss}$	-	35.0	-	pF
Output Capacitance		$C_{oss}$	-	10.0	-	
Reverse Transfer Capacitance		$C_{rss}$	-	8.5	-	
Turn-On Delay Time	$V_{DD} = 30\text{V}, V_{GS} = 10\text{V}, I_D = 0.5\text{A}, R_g = 25\Omega$	$t_{d(on)}$	-	3.8	-	ns
Turn-On Rise Time		$t_r$	-	3.4	-	
Turn-Off Delay Time		$t_{d(off)}$	-	19.0	-	
Turn-Off Fall Time		$t_f$	-	12.0	-	
<b>Drain-Source Body Diode</b>						
Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 0.5\text{A}$	$V_{SD}$	-	-	1.3	V
Continuous Source Current	-	$I_S$	-	-	0.57	A



# SMX06C7KDTH

## Complementary Pair Enhancement Mode MOSFET

### Electrical Characteristics ( $T_A = 25\text{ }^\circ\text{C}$ unless otherwise specified)

#### P-Channel Q2

Parameter	Conditions	Symbol	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain Source Breakdown Voltage	$I_D = -250\mu\text{A}$	$V_{(BR)DSS}$	-60	-	-	V
Zero Gate Voltage Drain Current	$V_{DS} = -48\text{V}$	$I_{DSS}$	-	-	-1	$\mu\text{A}$
Gate Source Leakage Current	$V_{GS} = \pm 20\text{V}$	$I_{GSS}$	-	-	$\pm 10$	$\mu\text{A}$
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	$V_{GS(th)}$	-1.0	-	-2.5	V
Static Drain Source On-Resistance	$V_{GS} = -10\text{V}, I_D = -0.2\text{A}$	$R_{DS(on)}$	-	-	6.0	$\Omega$
	$V_{GS} = -5\text{V}, I_D = -0.1\text{A}$		-	-	7.0	
<b>Dynamic</b>						
Total Gate Charge	$V_{DS} = -25\text{V}, V_{GS} = -4.5\text{V}, I_D = -0.1\text{V}$	$Q_g$	-	1.1	-	nC
Gate-Source Charge		$Q_{gs}$	-	0.3	-	
Gate-Drain Charge		$Q_{gd}$	-	0.2	-	
Input Capacitance	$V_{DS} = -25\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$	$C_{iss}$	-	38	-	pF
Output Capacitance		$C_{oss}$	-	10	-	
Reverse Transfer Capacitance		$C_{rss}$	-	6	-	
Turn-On Delay Time	$V_{DS} = -25\text{V}, V_{GS} = -10\text{V}, I_D = -0.1\text{A}, R_g = 6.8\Omega$	$t_{d(on)}$	-	14.0	-	ns
Turn-On Rise Time		$t_r$	-	4.3	-	
Turn-Off Delay Time		$t_{d(off)}$	-	15.0	-	
Turn-Off Fall Time		$t_f$	-	76.0	-	
<b>Drain-Source Body Diode</b>						
Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = -0.5\text{A}$	$V_{SD}$	-	-	-1.3	V
Continuous Source Current	-	$I_S$	-	-	-0.35	A



# SMX06C7KDTH

## Complementary Pair Enhancement Mode MOSFET

### RATINGS AND CHARACTERISTIC CURVES

#### N-Channel Q1

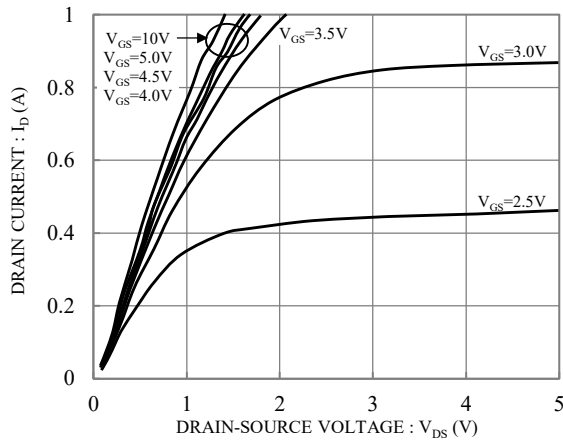


Fig.1 Typical Output Characteristics

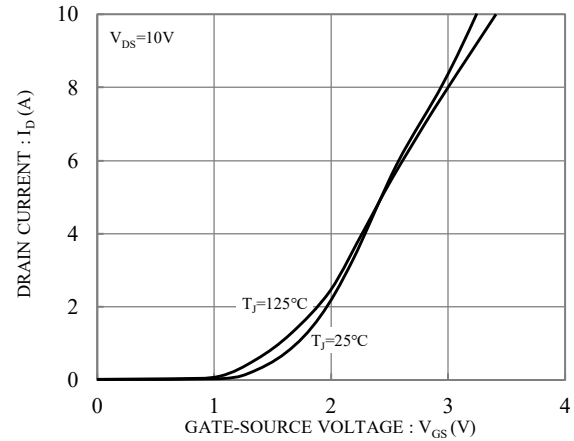


Fig.2 Typical Transfer Characteristics

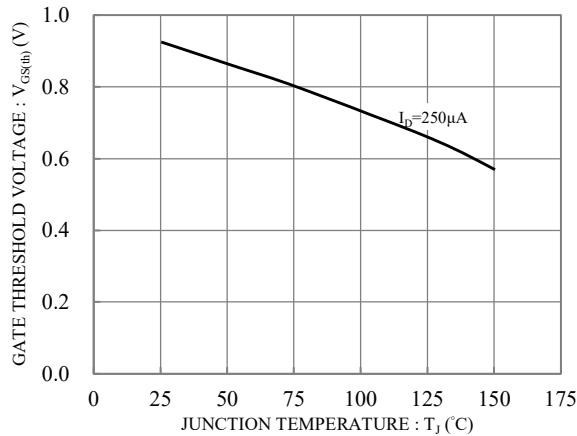


Fig.3 Gate Threshold Voltage vs. Junction Temperature

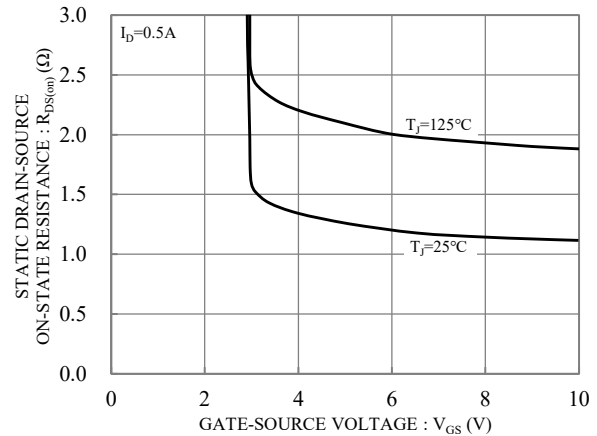


Fig.4 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

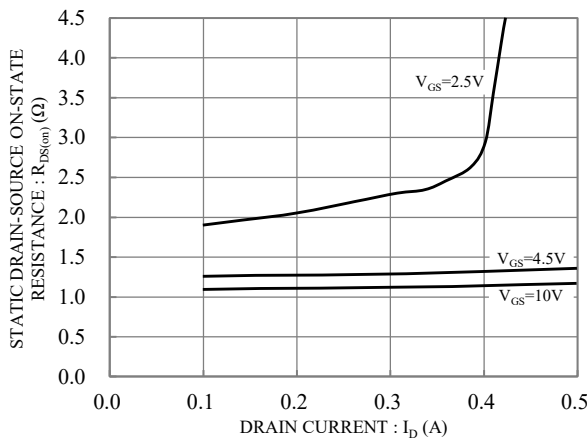


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

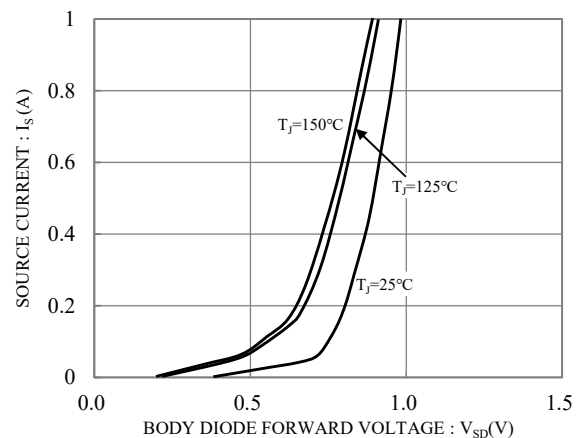
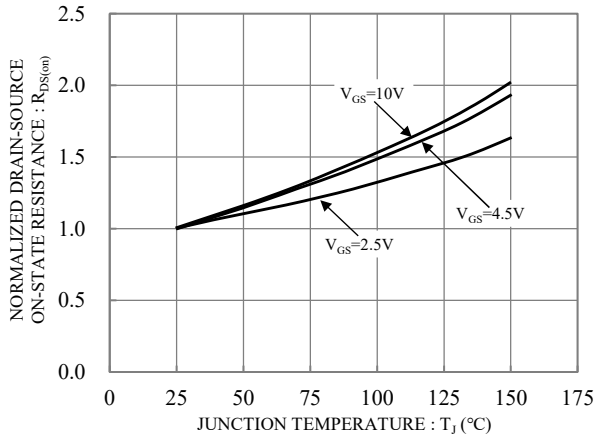


Fig.6 Body Diode Forward Voltage vs. Source Current

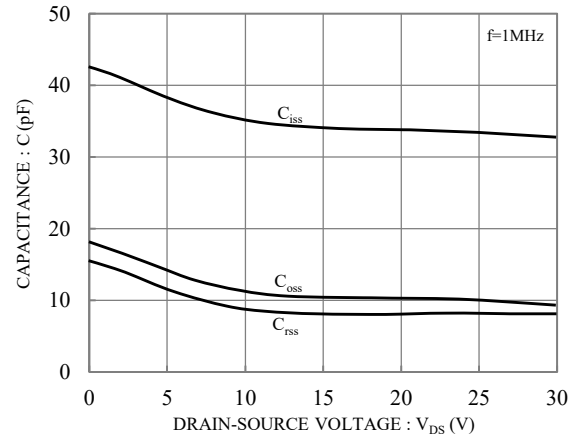


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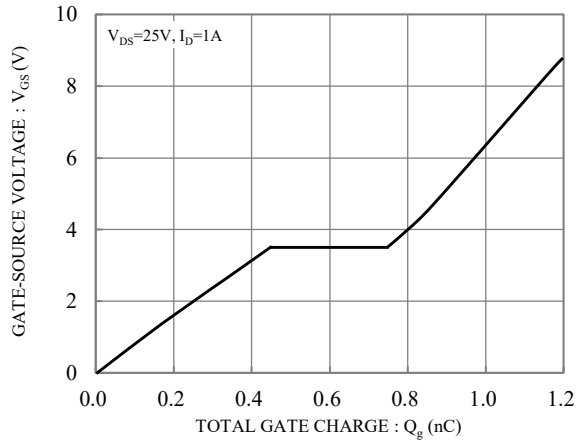
## Complementary Pair Enhancement Mode MOSFET



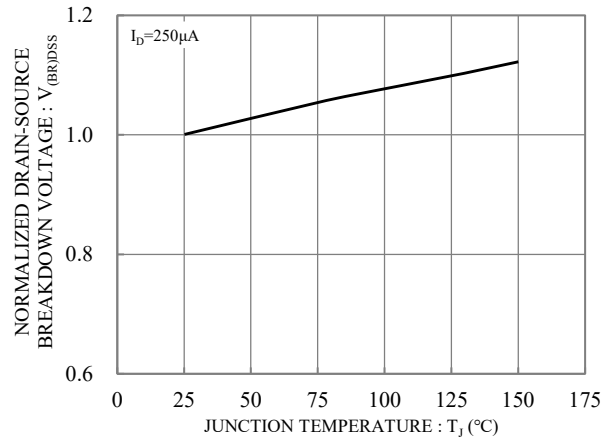
**Fig.7 Drain-Source On-State Resistance vs. Junction Temperature**



**Fig.8 Capacitance vs. Drain-Source Voltage**



**Fig.9 Gate Charge Characteristics**



**Fig.10 Breakdown Voltage vs. Junction Temperature**



# SMX06C7KDTH

## Complementary Pair Enhancement Mode MOSFET

### RATINGS AND CHARACTERISTIC CURVES

#### P-Channel Q2

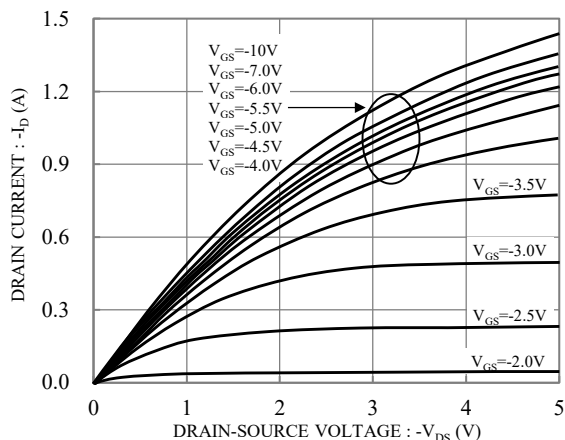


Fig.11 Typical Output Characteristics

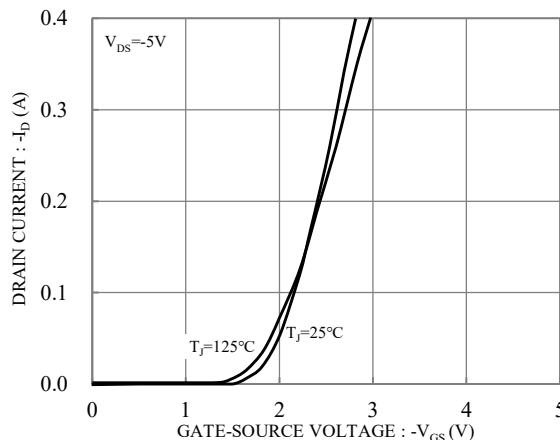


Fig.12 Typical Transfer Characteristics

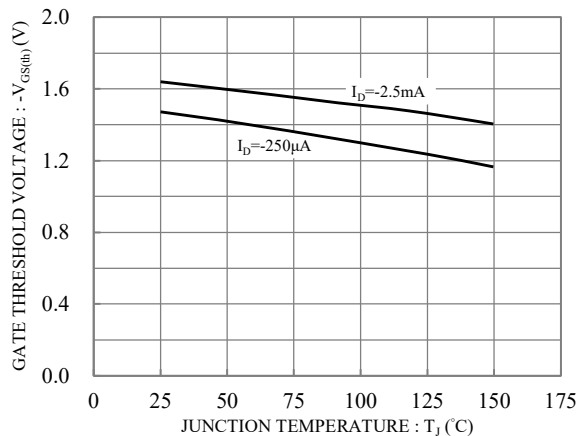


Fig.13 Gate Threshold Voltage vs. Junction Temperature

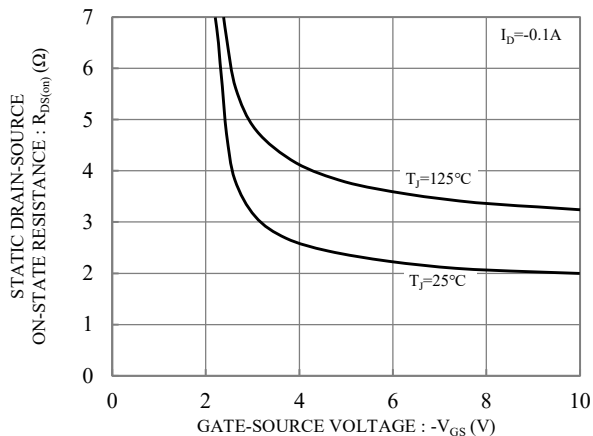


Fig.14 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

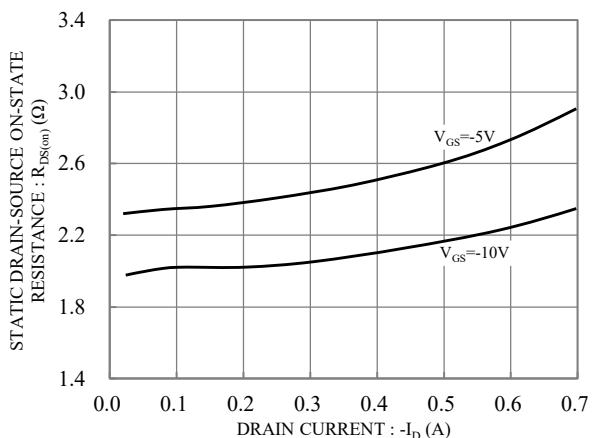


Fig.15 Static Drain-Source On-State Resistance vs. Drain Current

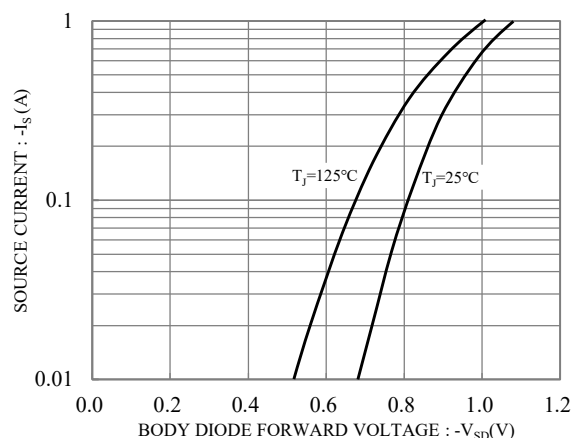
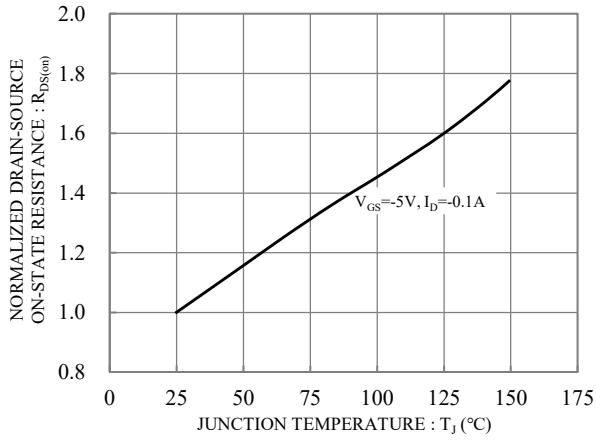


Fig.16 Body Diode Forward Voltage vs. Source Current

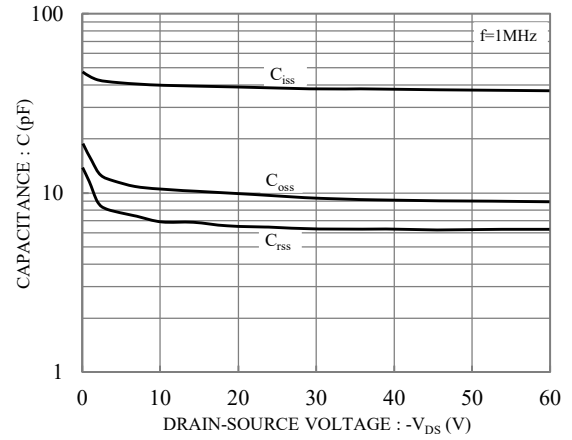


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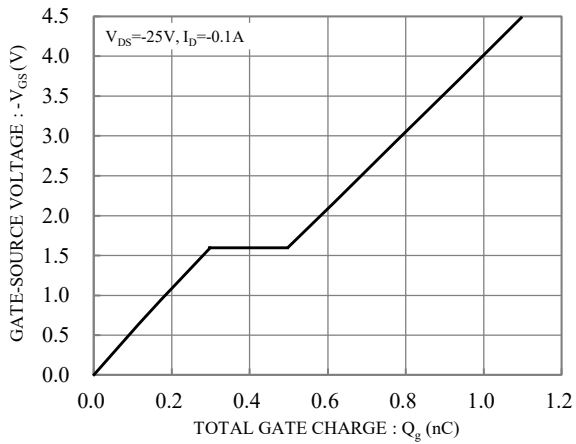
## Complementary Pair Enhancement Mode MOSFET



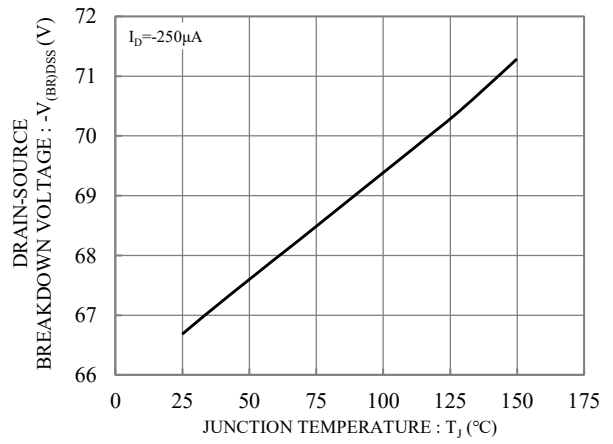
**Fig.17 Drain-Source On-State Resistance vs. Junction Temperature**



**Fig.18 Capacitance vs. Drain-Source Voltage**



**Fig.19 Gate Charge Characteristics**



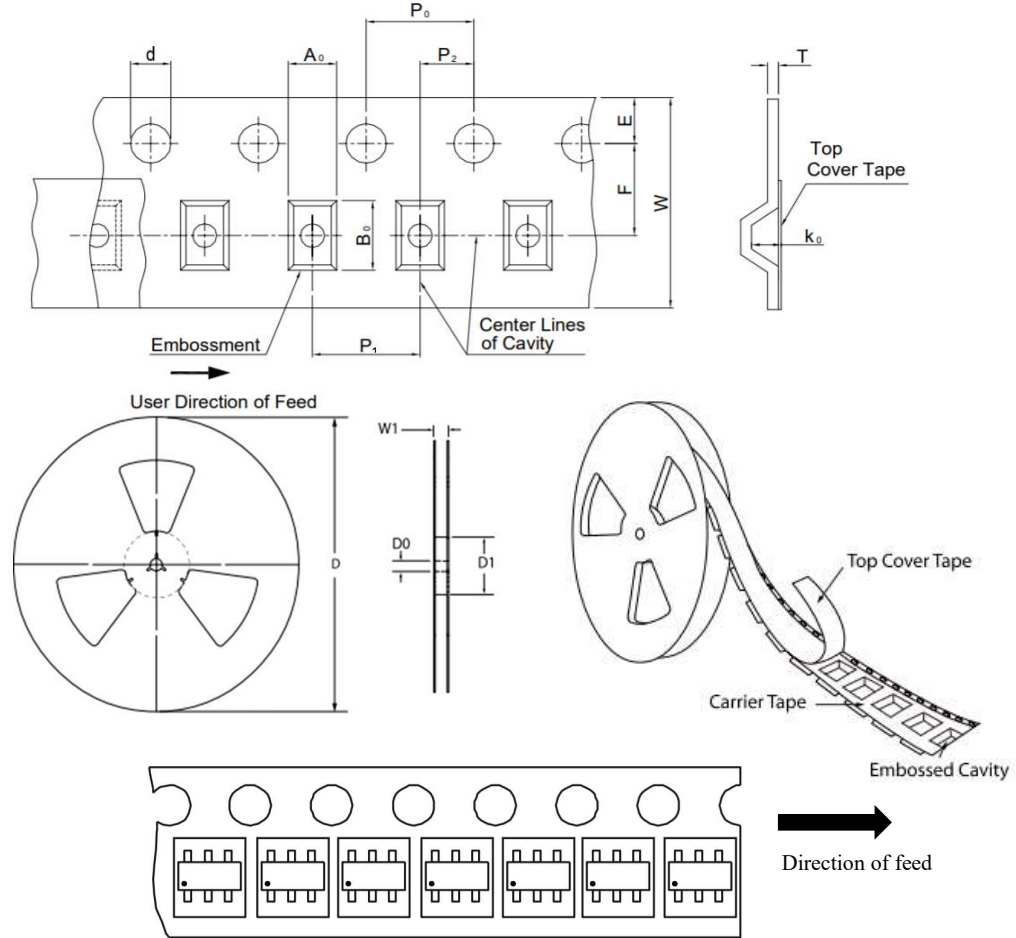
**Fig.20 Breakdown Voltage vs. Junction Temperature**



# SMX06C7KDTH

## Complementary Pair Enhancement Mode MOSFET

### TAPE & REEL SPECIFICATION



Item	Symbol	SOT-563
Carrier width	A <sub>0</sub>	1.80 ± 0.05
Carrier length	B <sub>0</sub>	1.80 ± 0.05
Carrier depth	K <sub>0</sub>	0.70 ± 0.05
Sprocket hole	d	1.50 ± 0.10
Reel outside diameter	D	178.00 ± 2.00
Feed hole width	D <sub>0</sub>	13.00 ± 0.50
Reel inner diameter	D <sub>1</sub>	MIN. 50.00
Sprocke hole position	E	1.75 ± 0.10
Punch hole position	F	3.50 ± 0.10
Sprocke hole pitch	P <sub>0</sub>	4.00 ± 0.10
Punch hole pitch	P <sub>1</sub>	4.00 ± 0.10
Embossment center	P <sub>2</sub>	2.00 ± 0.10
Overall tape thickness	T	0.60 ± 0.05
Tape width	W	8.00 ± 0.30
Reel width	W1	MAX. 14.50

### ORDER INFORMATION

Package	Reel Size	Quantity
SOT-563	7"	4,000

### MARKING CODE

Part Number	Marking Code
SMX06C7KDTH	MJ

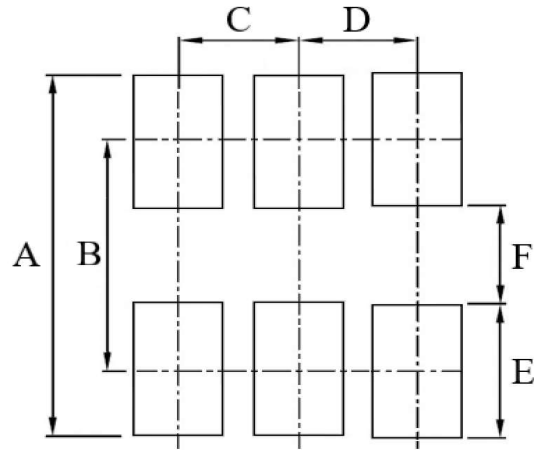




# SMX06C7KDTH

Complementary Pair Enhancement Mode MOSFET

## SUGGESTED SOLDER PAD LAYOUT



Unit : mm

PACKAGE	A	B	C	D	E	F
SOT-563	2.30	1.45	0.50	0.50	0.85	0.60