

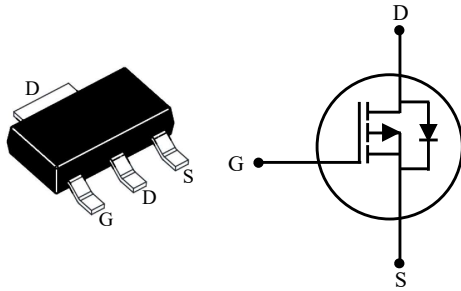


SMTQ06P2K0LSEH

P-Channel Enhancement Mode Field Effect Transistor

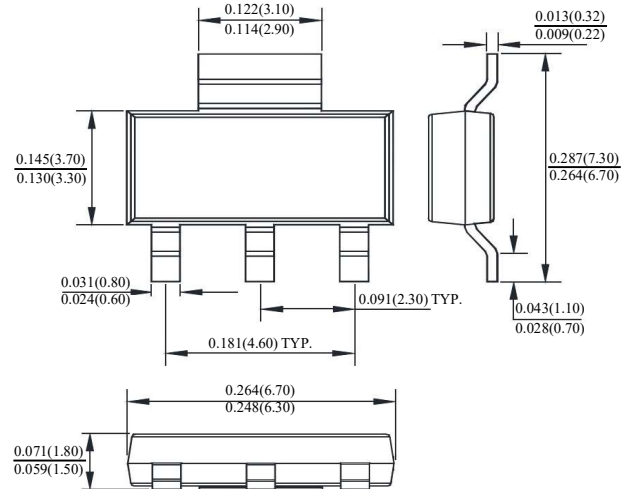
FEATURES

· Suffix "H" indicates Halogen-free parts, ex.SMTQ06P2K0LSEH.



D	Drain
G	Gate
S	Source

SOT-223



Dimensions in inch and (millimeter)

Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	V_{DS}	-60	V	
Gate-Source Voltage	V_{GS}	± 20	V	
Drain Current	I_D	$T_C = 25^\circ\text{C}$	-7.0	A
		$T_C = 100^\circ\text{C}$	-4.4	A
Pulsed Drain Current (Note 1)	I_{DM}	-15	A	
Single-Pulse Avalanche Current	I_{AS}	-9.3	A	
Single-Pulse Avalanche Energy (Note 2)	E_{AS}	4.3	mJ	
Power Dissipation	P_D	18.6	W	
Thermal Resistance from Junction to Ambient (Note 3)	$R_{\theta JA}$	65	$^\circ\text{C}/\text{W}$	
Thermal Resistance from Junction to Case	$R_{\theta JC}$	6.7	$^\circ\text{C}/\text{W}$	
Operating and Storage Temperature Range	T_J, T_{stg}	- 55 to + 150	$^\circ\text{C}$	

Note :

1. Pulse width $\leq 100\mu\text{s}$, Duty Cycle $\leq 2\%$, Repetitive rating, pulse width limited by junction temperature $T_{J(\text{MAX})} = 150^\circ\text{C}$.
2. Limited by $T_{J(\text{MAX})}$, starting $T_J = 25^\circ\text{C}$, $L = 0.1\text{mH}$, $R_g = 25\Omega$, $I_{AS} = -9.3\text{A}$, $V_{GS} = 10\text{V}$.
3. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch² copper plate in still air.



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Electrical Characteristics ($T_A = 25\text{ }^\circ\text{C}$ unless otherwise specified)

Parameter	Conditions	Symbol	Min.	Typ.	Max.	Unit
Static						
Drain Source Breakdown Voltage	$I_D = -250\mu\text{A}$	$V_{(BR)DSS}$	-60	-	-	V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	$V_{GS(th)}$	-1.2	-	-2.5	V
Zero Gate Voltage Drain Current	$V_{DS} = -60\text{V}$	I_{DSS}	-	-	-1	μA
Gate-Body Leakage Current	$V_{GS} = \pm 20\text{V}$	I_{GSS}	-	-	± 0.1	μA
Drain-Source On-State Resistance	$V_{GS} = -10\text{V}, I_D = -5\text{A}$	$R_{DS(on)}$	-	150	170	m Ω
	$V_{GS} = -4.5\text{V}, I_D = -4\text{A}$		-	-	220	
Dynamic						
Forward Transfer Admittance	$V_{DS} = -5\text{V}, I_D = -4\text{A}$	g_{FS}	-	5.5	-	S
Gate Resistance	$V_{GS} = 0\text{V}, V_{DS} = 0\text{V}, f = 1\text{MHz}$	R_g	-	9.2	-	Ω
Total Gate Charge	$V_{DS} = -30\text{V}, I_D = -5\text{A}, V_{GS} = -4.5\text{V}$	Q_g	-	3.2	-	nC
Gate-Source Charge			$V_{DS} = -30\text{V}, I_D = -5\text{A}, V_{GS} = -10\text{V}$	-	7.0	
Gate-Drain Charge	Q_{gs}	-		1.6	-	
Input Capacitance	Q_{gd}	-		1.3	-	
Output Capacitance	$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$	C_{iss}	-	361	-	pF
Reverse Transfer Capacitance		C_{oss}	-	25	-	
Turn-On Delay Time		C_{rss}	-	20	-	
Turn-On Rise Time	$V_{DD} = -30\text{V}, V_{GS} = -10\text{V}, I_D = -5\text{A}, R_g = 3.3\Omega$	$t_{d(on)}$	-	5.0	-	ns
Turn-On Delay Time		t_r	-	3.0	-	
Turn-Off Delay Time		$t_{d(off)}$	-	7.5	-	
Turn-Off Fall Time		t_f	-	1.0	-	
Drain-Source Body Diode						
Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = -1\text{A}$	V_{SD}	-	-	1.2	V
Diode Continuous Forward Current	-	I_S	-	-	7	A
Diode Pulse Current	-	I_{SM}	-	-	15	A
Reverse Recovery Time	$I_S = -5\text{A}, di/dt = 100\text{A}/\mu\text{s}$	t_{rr}	-	8	-	ns
Reverse Recovery Charge		Q_{rr}	-	3.6	-	nC



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RATINGS AND CHARACTERISTIC CURVES

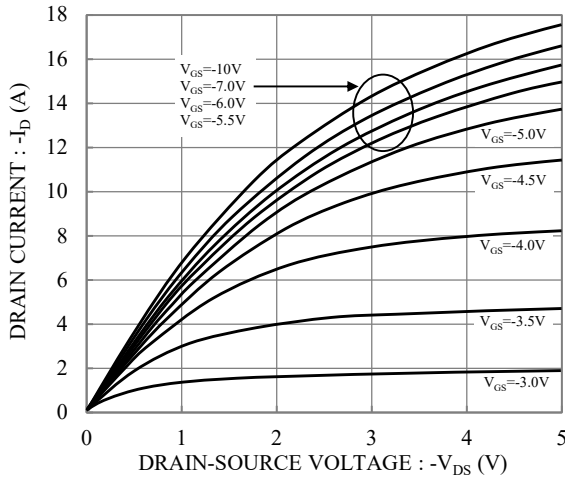


Fig.1 Typical Output Characteristics

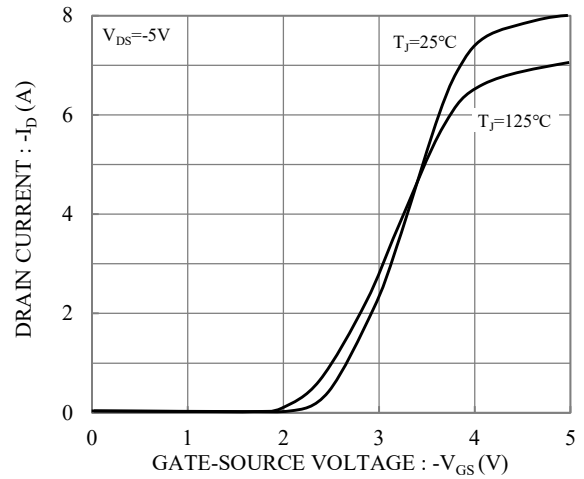


Fig.2 Typical Transfer Characteristics

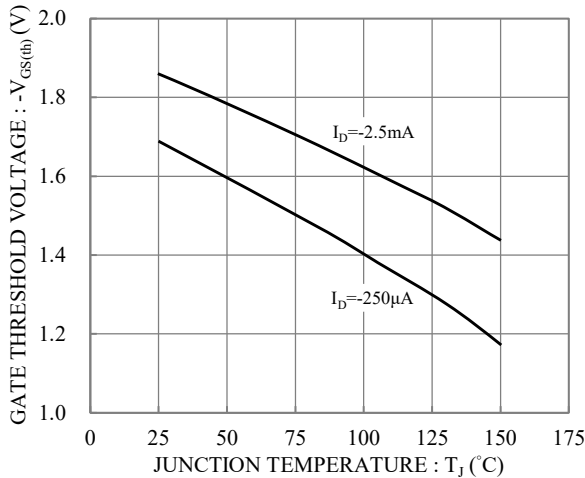


Fig.3 Gate Threshold Voltage vs. Junction Temperature

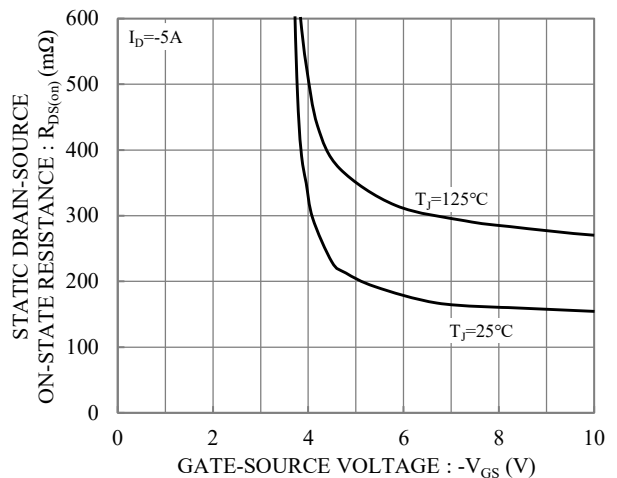


Fig.4 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

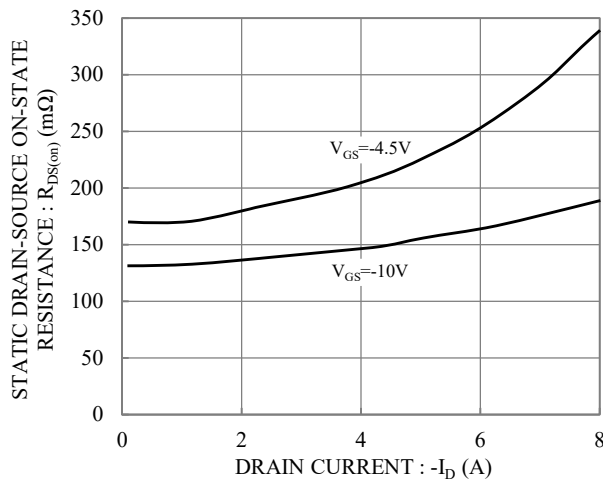


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

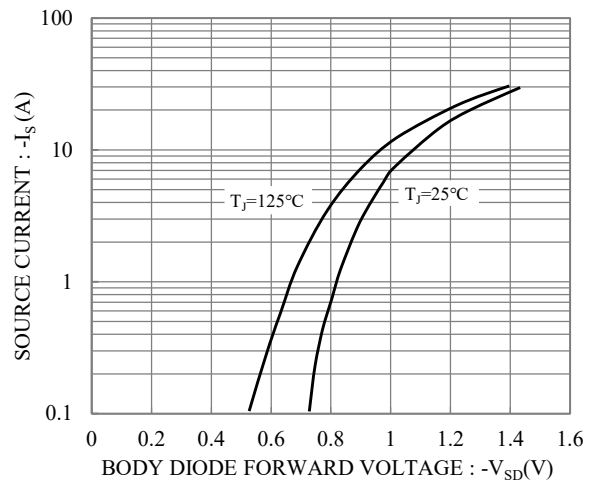


Fig.6 Body Diode Forward Voltage vs. Source Current



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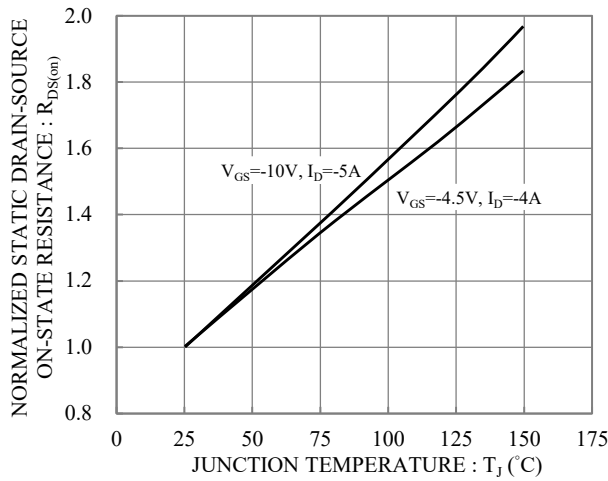


Fig.7 Drain-Source On-State Resistance vs. Junction Temperature

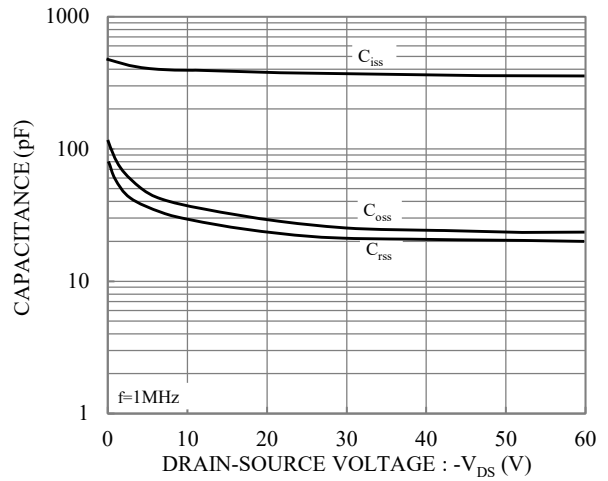


Fig.8 Capacitance vs. Drain-Source Voltage

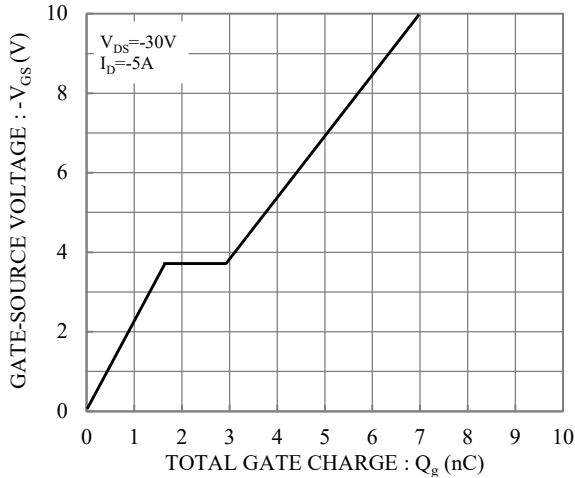


Fig.9 Gate Charge

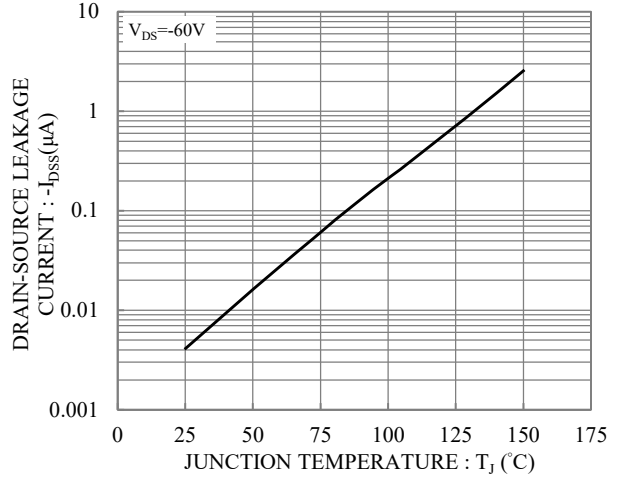


Fig.10 Drain-Source Leakage Current vs. Junction Temperature

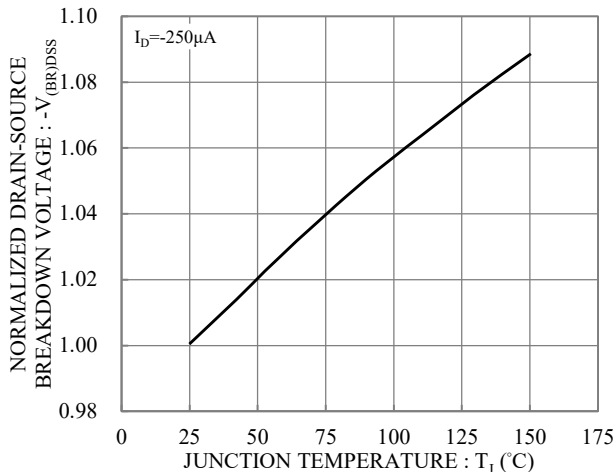


Fig.11 Brekdown Voltage vs. Junction Temperature

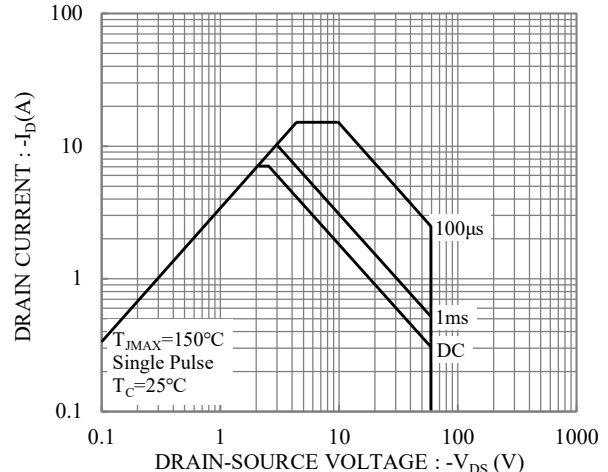


Fig.12 Safe Operation Area



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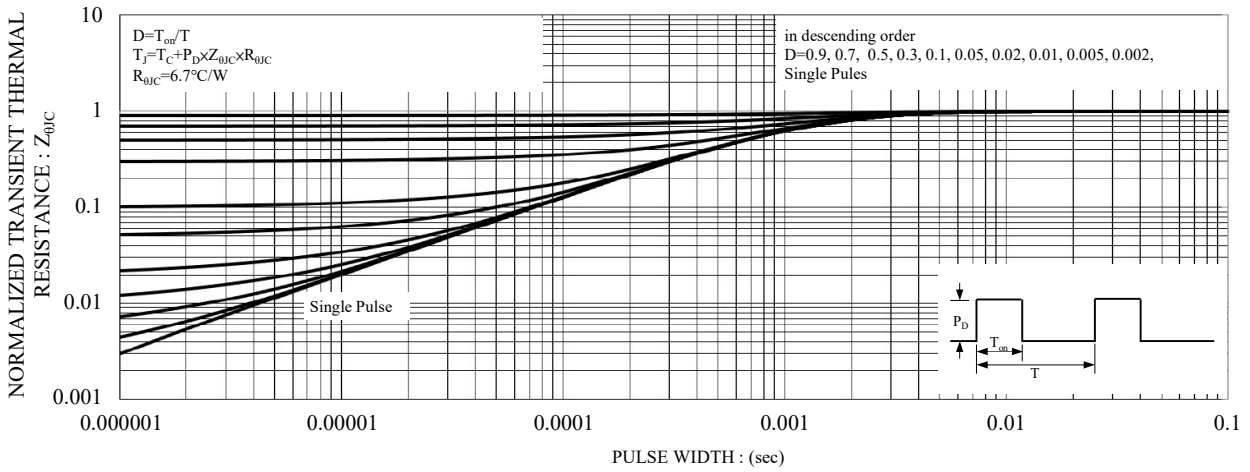


Fig.13 Maximum Transient Thermal Impedance

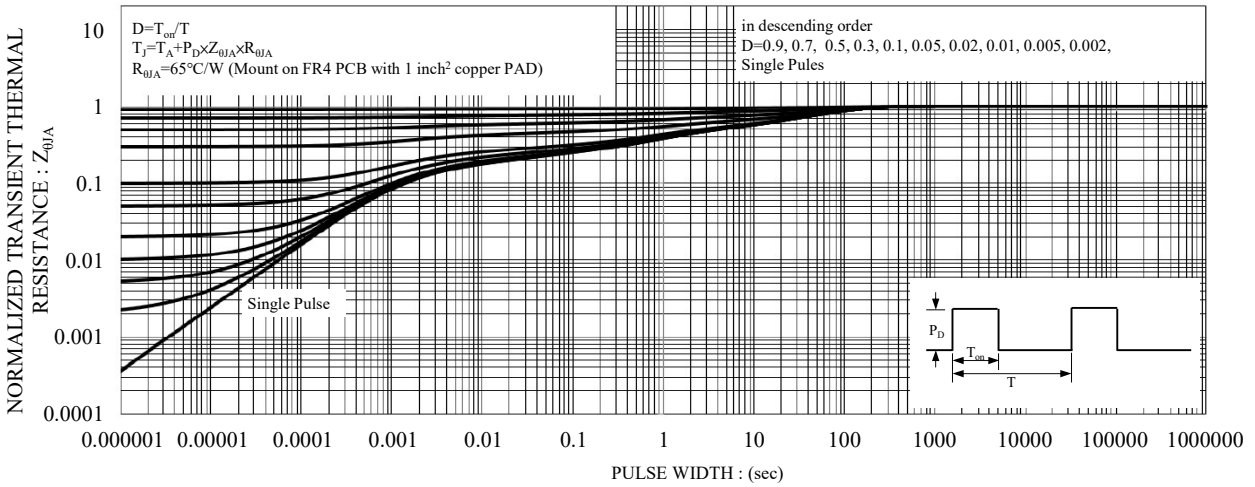


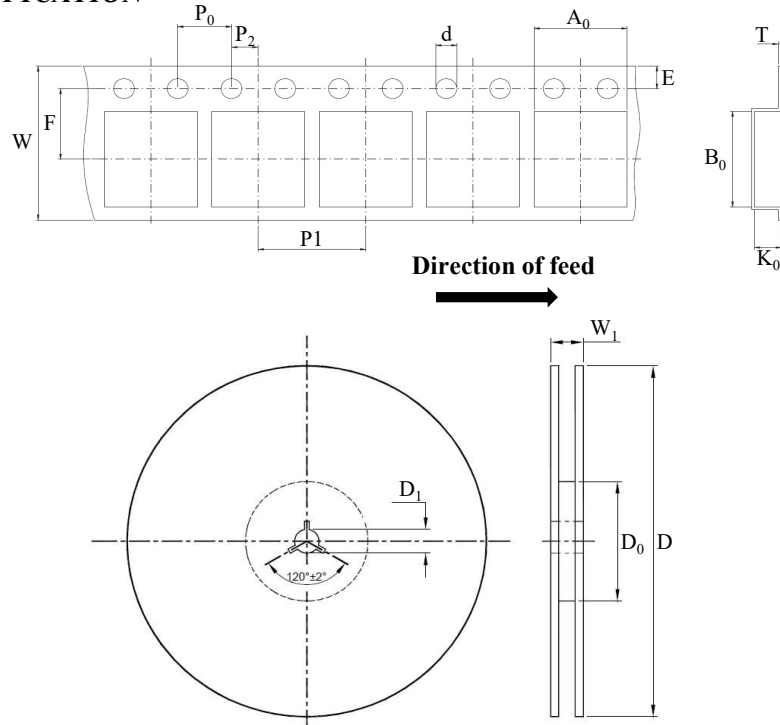
Fig.14 Maximum Transient Thermal Impedance



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TAPE & REEL SPECIFICATION



Item	Symbol	SOT-223
Carrier width	A ₀	7.05 ± 0.10
Carrier length	B ₀	7.45 ± 0.10
Carrier depth	K ₀	1.95 ± 0.10
Sprocket hole	d	1.50 ± 0.10
Reel outside diameter	D	330.00 ± 2.00
Feed hole width	D ₀	100.00
Reel inner diameter	D ₁	16.40 ± 0.50
Sprocket hole position	E	1.75 ± 0.10
Punch hole position	F	5.50 ± 0.10
Sprocket hole pitch	P ₀	4.00 ± 0.10
Punch hole pitch	P ₁	8.00 ± 0.10
Embossment center	P ₂	2.00 ± 0.10
Overall tape thickness	T	0.25 ± 0.05
Tape width	W	12.00 ± 0.20
Reel width	W ₁	MAX. 20.00

ORDER INFORMATION

Part Number	Marking Code	Reel Size	Quantity
SMTQ06P2K0LSEH	TQ06P2K0L	13"	3,000



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SUGGESTED SOLDER PAD LAYOUT

