

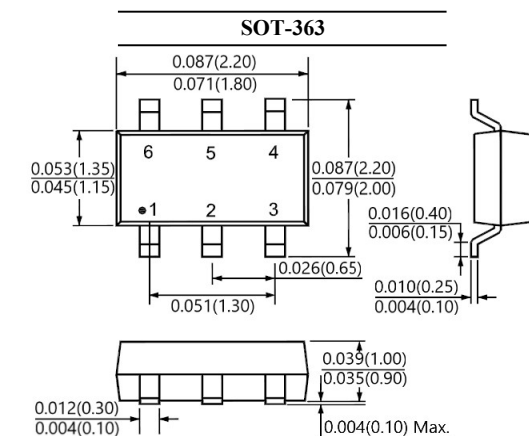
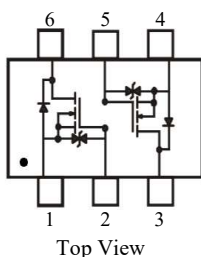
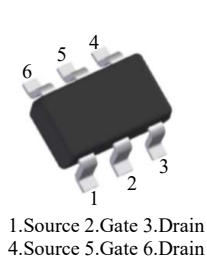


# MMBT7002KDW H

## N-Channel Enhancement Mode Field Effect Transistor

### FEATURES

- Low on resistance  $R_{DS(ON)}$
- Low gate threshold voltage
- ESD protected up to 2KV
- Suffix "H" indicates Halogen-free parts, ex. MMBT7002KDW H



Dimensions in inch and (millimeter)

### Maximum Ratings ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	60	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current (Continuous)	$I_D$	300	mA
Drain Current (Pulse Width $\leq 10 \mu\text{s}$ )	$I_{DM}$	1.2	A
Total Power Dissipation	(Note 1)	295	mW
	(Note 2)	350	
Thermal Resistance from Junction to Ambient	(Note 1)	423	$^\circ\text{C}/\text{W}$
	(Note 2)	357	
Operating and Storage Temperature Range	$T_j, T_{stg}$	- 55 to + 150	$^\circ\text{C}$

Note:

1. Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
2. Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain  $1 \text{ cm}^2$ .



# MMBT7002KDW

## N-Channel Enhancement Mode Field Effect Transistor

### Electrical Characteristics ( $T_A = 25^\circ\text{C}$ unless otherwise specified)

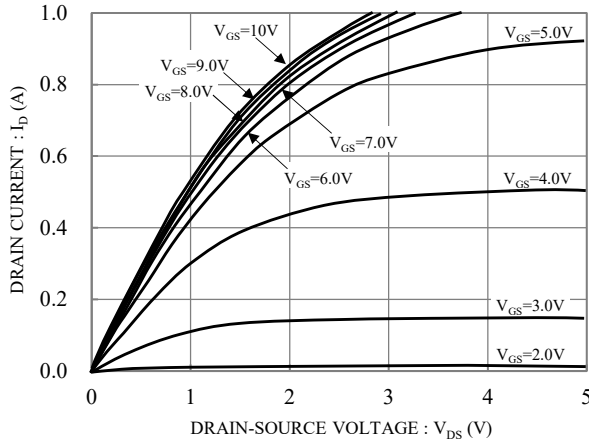
Parameter	Conditions	Symbol	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain Source Breakdown Voltage	$I_D = 10\mu\text{A}$	$BV_{DSS}$	60	-	-	V
Zero Gate Voltage Drain Current	$V_{DS} = 60\text{V}$	$I_{DSS}$	-	-	1	$\mu\text{A}$
Gate Source Leakage Current	$V_{GS} = \pm 20\text{V}$	$I_{GSS}$	-	-	$\pm 10$	$\mu\text{A}$
Gate Threshold Voltage	$V_{DS} = 10\text{V}, I_D = 250\mu\text{A}$	$V_{GS(th)}$	1.10	-	1.75	V
Static Drain Source On-Resistance	$V_{GS} = 10\text{V}, I_D = 500\text{mA}$	$R_{DS(ON)}$	-	-	3	$\Omega$
	$V_{GS} = 5\text{V}, I_D = 50\text{mA}$		-	-	4	
Forward Transconductance	$V_{DS} = 10\text{V}, I_D = 200\text{mA}$	$g_{FS}$	80	-	-	mS
<b>Dynamic</b>						
Gate Resistance	$V_{DS} = 0\text{V}, V_{GS} = 0, f = 1\text{MHz}$	$R_g$	-	200	-	$\Omega$
Total Gate Charge	$V_{DS} = 10\text{V}, I_D = 0.5\text{A}, V_{GS} = 4.5\text{V}$	$Q_g$	-	0.44	-	nC
Gate-Source Charge		$Q_{gs}$	-	0.20	-	
Gate-Drain Charge		$Q_{gd}$	-	0.10	-	
Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1\text{MHz}$	$C_{iss}$	-	21.00	-	pF
Output Capacitance		$C_{oss}$	-	12.00	-	
Reverse Transfer Capacitance		$C_{rss}$	-	0.35	-	
Turn-On Delay Time	$V_{DS} = 30\text{V}, V_{GS} = 10\text{V}, I_D = 0.5\text{A}, R_g = 25\Omega$	$t_{d(on)}$	-	2.7	-	ns
Turn-On Rise Time		$t_r$	-	2.5	-	
Turn-Off Delay Time		$t_{d(off)}$	-	13.0	-	
Turn-Off Fall Time		$t_f$	-	8.0	-	
<b>Drain-Source Body Diode</b>						
Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 0.5\text{A}$	$V_{SD}$	-	0.85	-	V
Reverse Recovery Time	$I_S = 0.5\text{A}, di/dt = 100\text{A}/\mu\text{s}$	$t_{rr}$	-	30	-	ns
Reverse Recovery Charge		$Q_{rr}$	-	29	-	nC



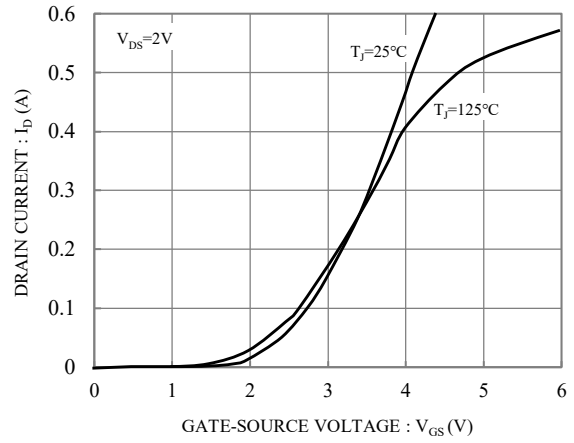
# MMBT7002KDW

## N-Channel Enhancement Mode Field Effect Transistor

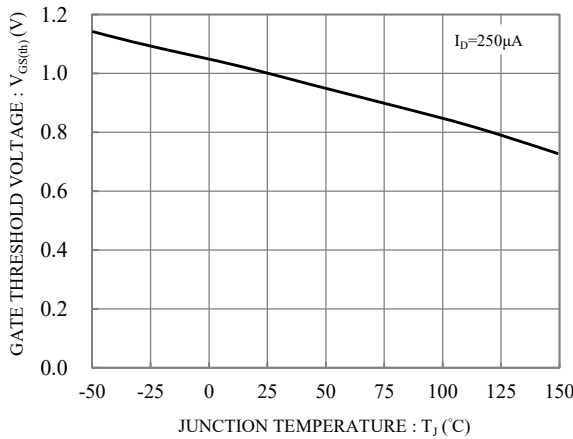
### RATINGS AND CHARACTERISTIC CURVES



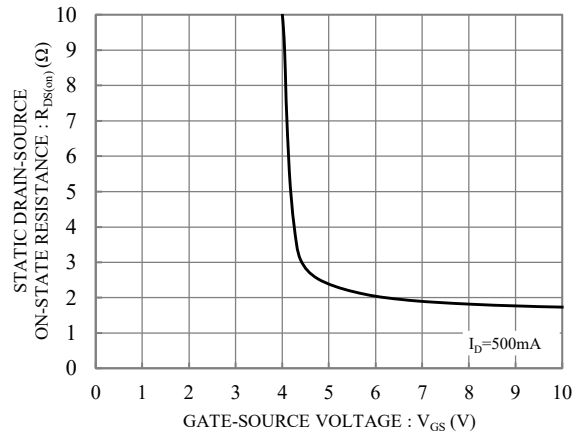
**Fig.1 Typical Output Characteristics**



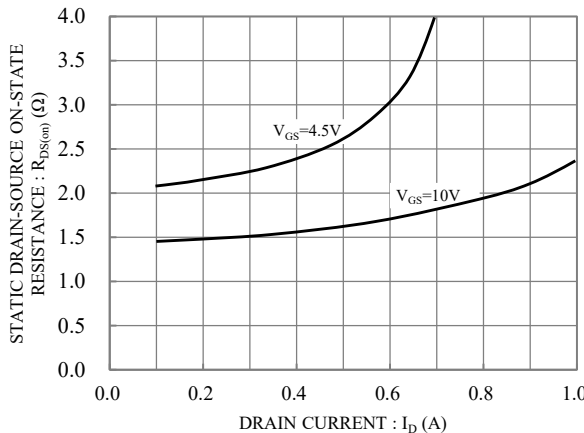
**Fig.2 Typical Transfer Characteristics**



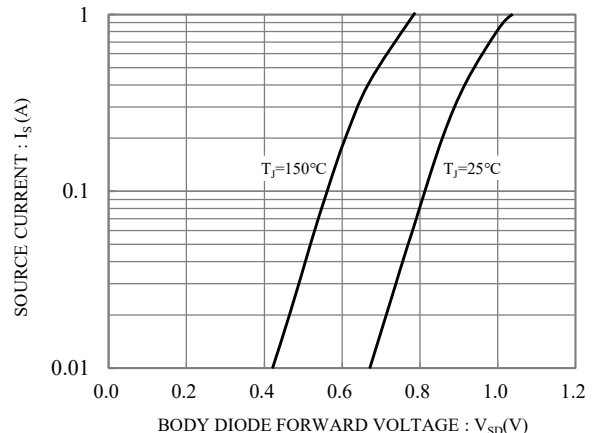
**Fig.3 Gate Threshold Voltage vs. Junction Temperature**



**Fig.4 Static Drain-Source On-State Resistance vs. Gate-Source Voltage**



**Fig.5 Static Drain-Source On-State Resistance vs. Drain Current**

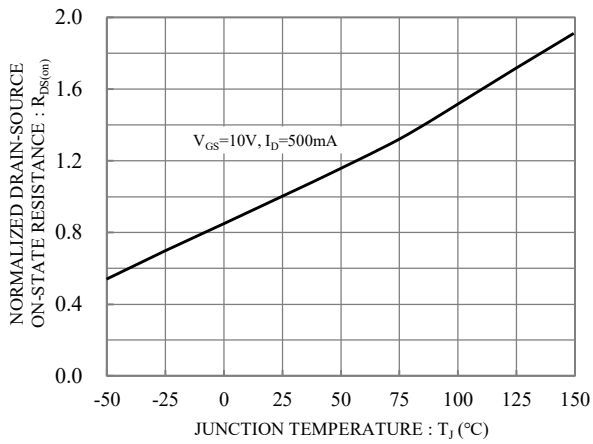


**Fig.6 Body Diode Forward Voltage vs. Source Current**

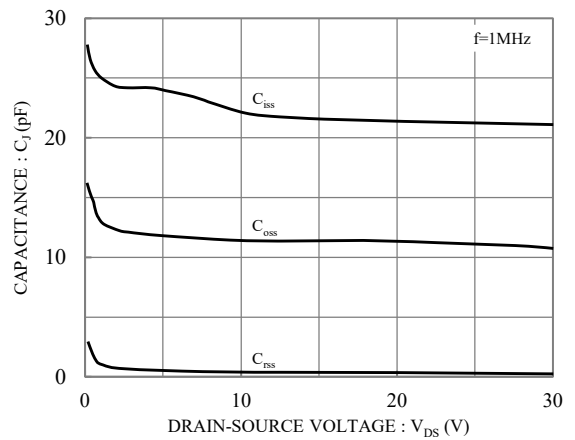


# MMBT7002KDWH

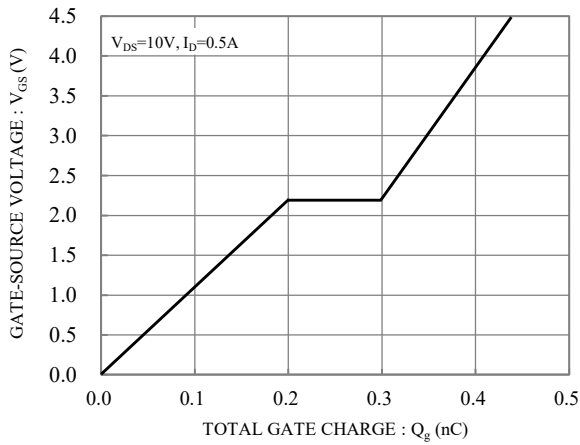
## N-Channel Enhancement Mode Field Effect Transistor



**Fig.7 Drain-Source On-State Resistance vs. Junction Temperature**



**Fig.8 Capacitance vs. Drain-Source Voltage**



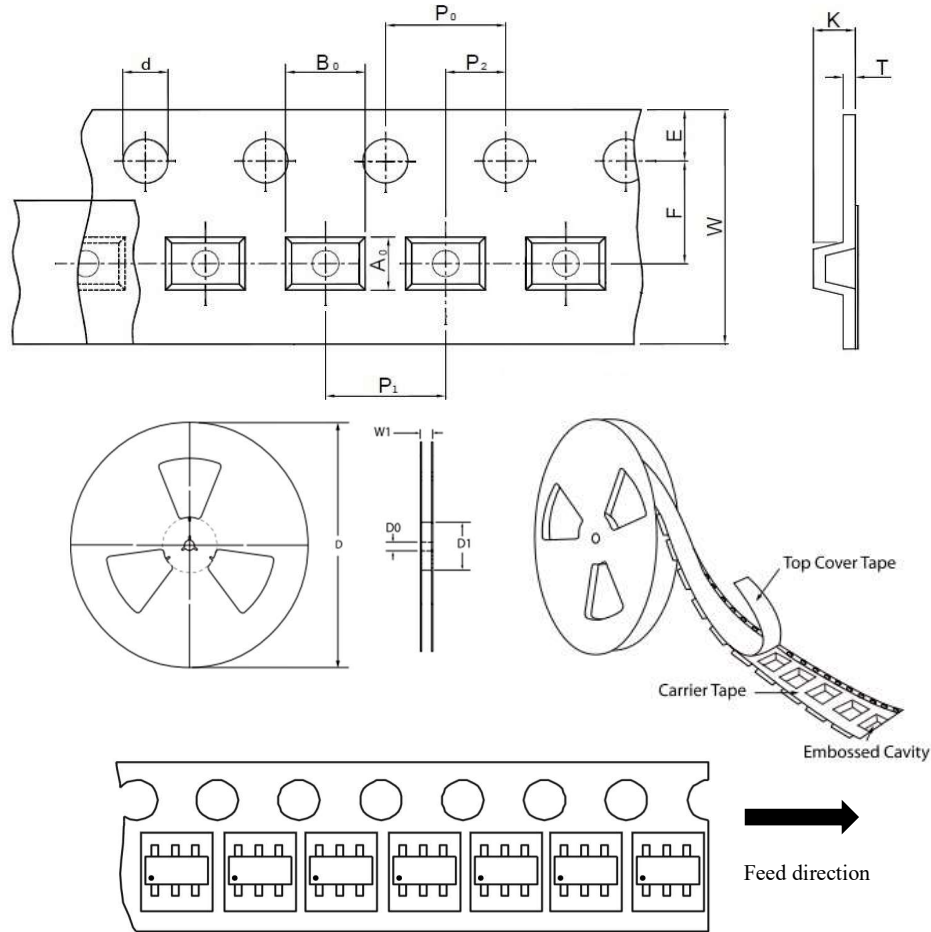
**Fig.9 Gate Charge Characteristics**



# MMBT7002KDWH

*N-Channel Enhancement Mode Field Effect Transistor*

## TAPE & REEL SPECIFICATION



Item	Symbol	SOT-363
Carrier width	A <sub>0</sub>	2.30 ± 0.10
Carrier length	B <sub>0</sub>	2.30 ± 0.10
Carrier depth	K <sub>0</sub>	1.20 ± 0.10
Sprocket hole	d	1.50 ± 0.10
Reel outside diameter	D	178.00 ± 2.00
Feed hole width	D <sub>0</sub>	13.00 ± 0.50
Reel inner diameter	D <sub>1</sub>	MIN. 50.00
Sprocke hole position	E	1.75 ± 0.10
Punch hole position	F	3.50 ± 0.10
Sprocke hole pitch	P <sub>0</sub>	4.00 ± 0.10
Punch hole pitch	P <sub>1</sub>	4.00 ± 0.10
Embossment center	P <sub>2</sub>	2.00 ± 0.10
Overall tape thickness	T	0.60 ± 0.10
Tape width	W	8.00 ± 0.30
Reel width	W1	MAX. 10.00

## ORDER INFORMATION

Package	Reel Size	Quantity
SOT-363	7"	3,000

## MARKING CODE

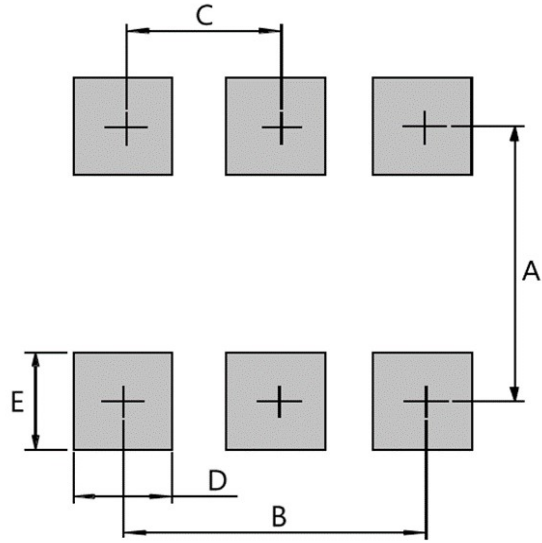
Part Number	Marking Code
MMBT7002KDWH	K7



# MMBT7002KDW

*N-Channel Enhancement Mode Field Effect Transistor*

## **SUGGESTED SOLDER PAD LAYOUT**



Unit : mm

PACKAGE	A	B	C	D	E
SOT-363	1.90	1.30	0.65	0.42	0.60