

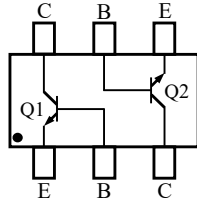
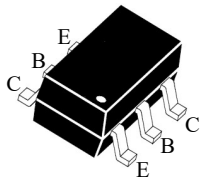


# ABC847 ADWH / BDWH / CDWH

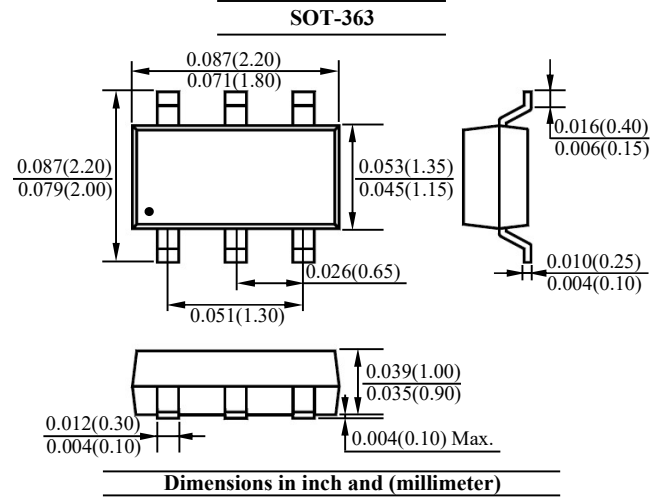
## DUAL NPN TRANSISTORS

### FEATURES

- AEC-Q101 qualified
- Suffix "H" indicates Halogen-free parts, ex. ABC847ADWH.



B	Base
C	Collector
E	Emitter



### NPN Maximum Ratings ( $T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Collector Base Voltage	$V_{CBO}$	50	V
Collector Emitter Voltage	$V_{CEO}$	45	V
Emitter Base Voltage	$V_{EBO}$	6	V
Collector Current	$I_C$	100	mA
Peak Collector Current	$I_{CM}$	200	mA
Power Dissipation	$P_D$	250	mW
Thermal Resistance from Junction to Ambient (Note 1)	$R_{\theta JA}$	500	$^\circ\text{C}/\text{W}$
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	- 55 to + 150	$^\circ\text{C}$

Note:

1. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



# ABC847 ADWH / BDWH / CDWH

## DUAL NPN TRANSISTORS

### NPN Electrical Characteristics ( $T_A = 25^\circ\text{C}$ unless otherwise specified)

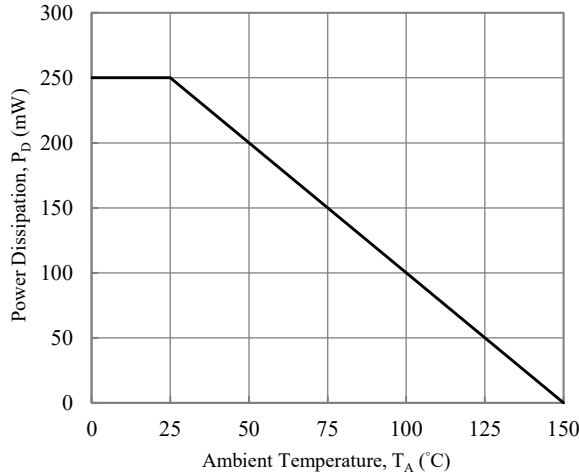
Parameter	Conditions	Symbol	Min.	Max.	Unit
DC Current Gain	A	$V_{CE}=5V, I_C=2mA$	110	220	-
	B		200	450	
	C		420	800	
Collector Base Cutoff Current	$V_{CB}=30V$	$I_{CBO}$	-	15	nA
Emitter Base Cutoff Current	$V_{EB}=5V$	$I_{EBO}$	-	100	nA
Collector Base Breakdown Voltage	$I_C=10\mu A$	$V_{(BR)CBO}$	50	-	V
Collector Emitter Breakdown Voltage	$I_C=10mA$	$V_{(BR)CEO}$	45	-	V
Emitter Base Breakdown Voltage	$I_E=1\mu A$	$V_{(BR)EBO}$	6	-	V
Collector Emitter Saturation Voltage	$I_C=10mA, I_B=0.5mA$	$V_{CE(sat)}$	-	0.25	V
	$I_C=100mA, I_B=5mA$		-	0.60	
Base Emitter Voltage	$V_{CE}=5V, I_C=2mA$	$V_{BE}$	0.58	0.70	V
	$V_{CE}=5V, I_C=10mA$		-	0.77	
Transition Frequency	$V_{CE}=5V, I_C=10mA, f=100MHz$	$f_T$	100	-	MHz
Collector Output Capacitance	$V_{CB}=10V, I_E=0V, f=1MHz$	$C_{ob}$	-	4.5	pF



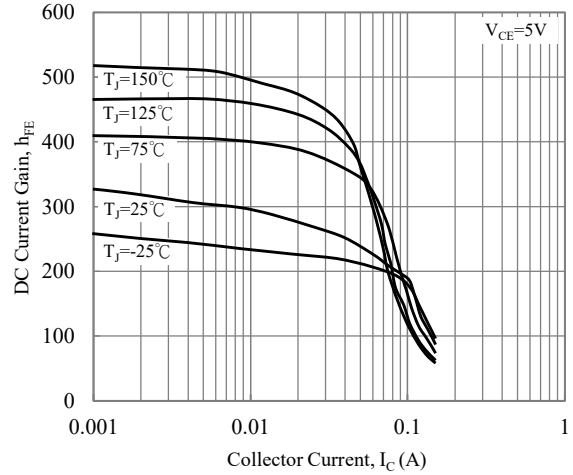
# ABC847 ADWH / BDWH / CDWH

## DUAL NPN TRANSISTORS

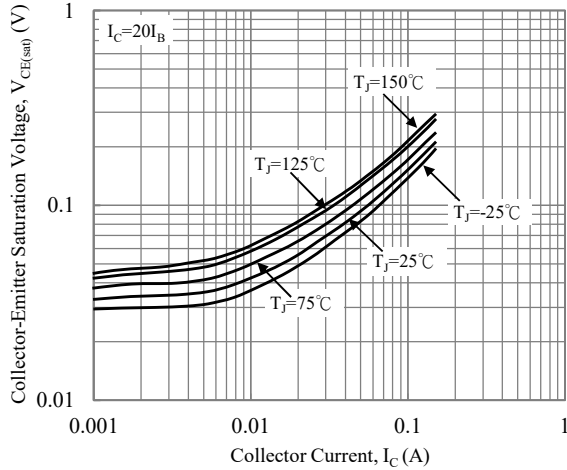
### RATINGS AND CHARACTERISTIC CURVES



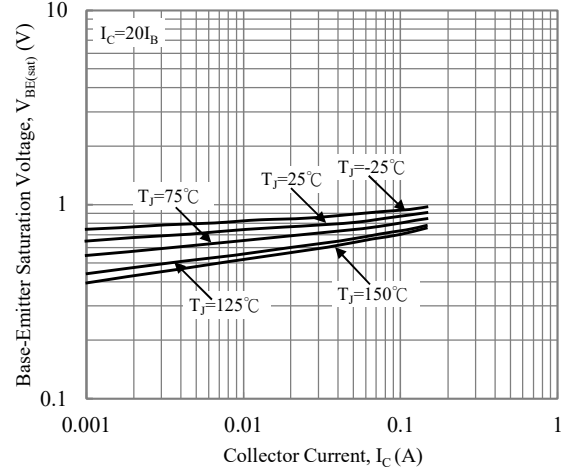
**Fig. 1 Power Derating Curves**



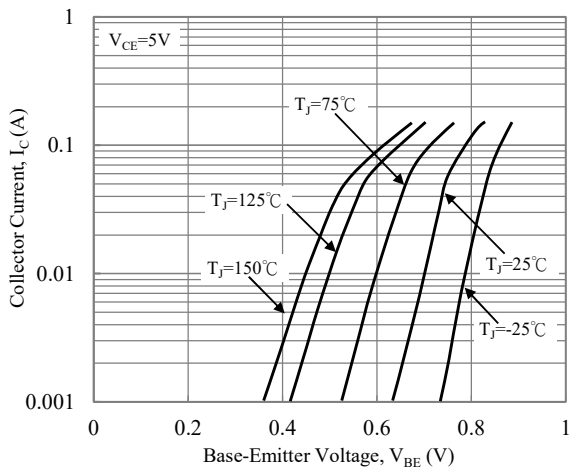
**Fig. 2 Current Gain vs. Collector Current**



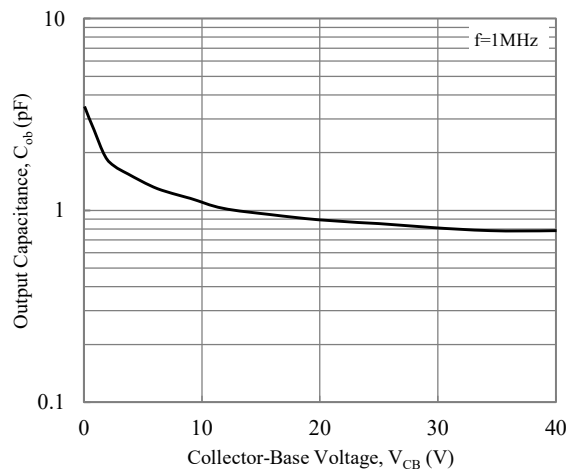
**Fig. 3 Collector-Emitter Saturation Voltage vs. Collector Current**



**Fig. 4 Base-Emitter Saturation Voltage vs. Collector Current**



**Fig. 5 Base-Emitter Voltage vs. Collector Current**

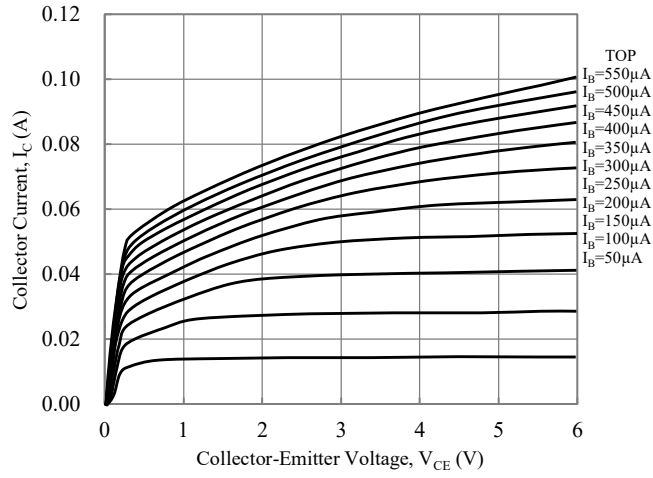


**Fig. 6 Output Capacitance**



# ABC847 ADWH / BDWH / CDWH

## DUAL NPN TRANSISTORS



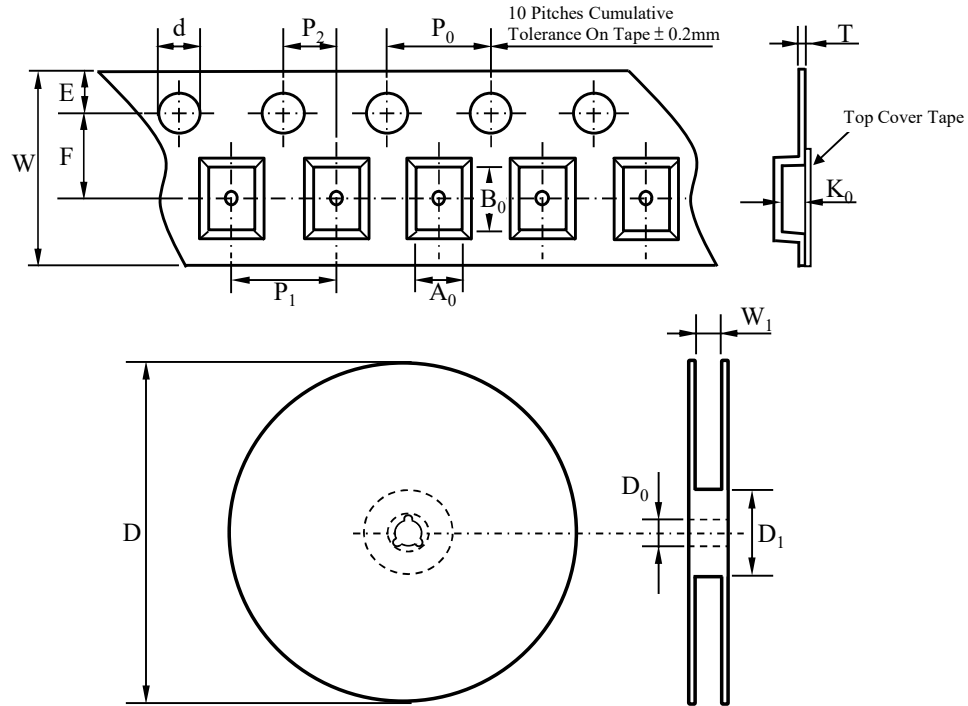
**Fig. 7 Output Characteristics Curves**



# ABC847 ADWH / BDWH / CDWH

## DUAL NPN TRANSISTORS

### TAPE & REEL SPECIFICATION



Item	Symbol	SOT-363
Carrier width	$A_0$	*
Carrier length	$B_0$	
Carrier depth	$K_0$	
Sprocket hole	d	$1.50 \pm 0.10$
Reel outside diameter	D	$178.00 \pm 2.00$
Feed hole width	$D_0$	$13.00 \pm 0.50$
Reel inner diameter	$D_1$	MIN. 50.00
Sprocket hole position	E	$1.75 \pm 0.10$
Punch hole position	F	$3.50 \pm 0.10$
Sprocket hole pitch	$P_0$	$4.00 \pm 0.10$
Punch hole pitch	$P_1$	$4.00 \pm 0.10$
Embossment center	$P_2$	$2.00 \pm 0.10$
Overall tape thickness	T	$0.60 \pm 0.10$
Tape width	W	$8.00 \pm 0.30$
Reel width	W1	MAX. 10.00

Note \*:  $A_0$ ,  $B_0$ , and  $K_0$  are determined by component size. The clearance between the components and the cavity must be within 0.05 mm min. to 0.5 mm max.

### ORDER INFORMATION

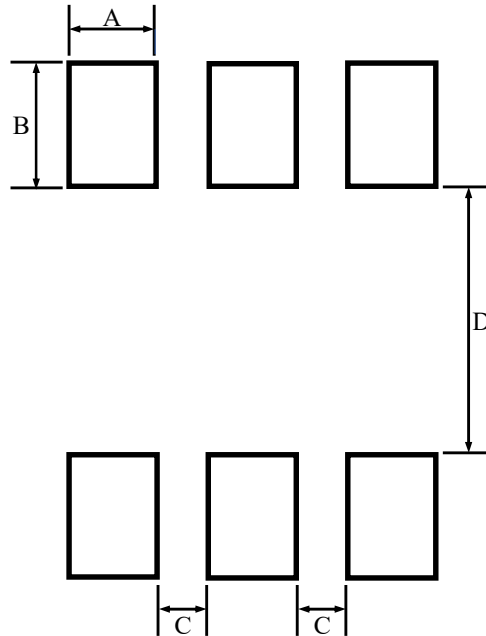
Part Number	Marking Code	Reel Size	Quantity
ABC847ADWH	1E	7"	3,000
ABC847BDWH	1F		
ABC847CDWH	1G		



# ABC847 ADWH / BDWH / CDWH

## DUAL NPN TRANSISTORS

### SUGGESTED SOLDER PAD LAYOUT



Unit :mm

PACKAGE	A	B	C	D
SOT-363	0.42	0.60	0.23	1.30