

FEATURES

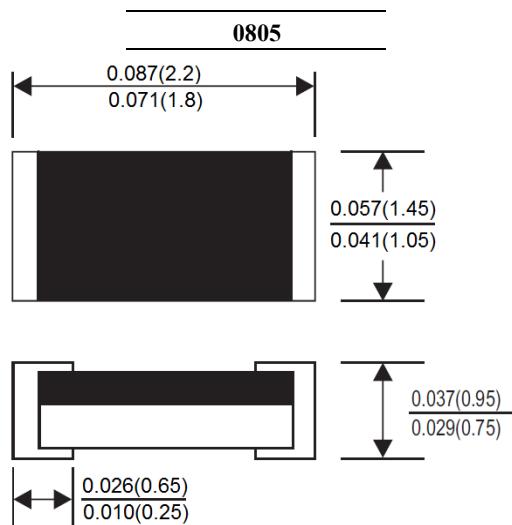
- Silicon epitaxial planar diode
- SMD chip pattern, available in various dimension included 1206 & 0603
- Leadfree and RoHS compliance components
- For small signal switching and operating ambient temperature less than 55 °C and voltage withstand less than 60V; not suitable for AC switching input as rectified circuit and high reverse voltage location.
- CM4148WSN is suitable for those application
- Suffix "H" indicates Halogen-free parts, ex. CM4148WSN

Mechanical Data

Case: 0805

Weight: approx. 6mg

Marking: Cathode band



Dimensions in inches and (millimeter)

Thermal Characteristics¹⁾ @ $T_A = 25^\circ C$, unless otherwise specified

Parameter	Symbol	Value	Unit
Forward Power Dissipation	P_{tot}	200	mW
Power derating above 25°C		1.6	mW/ °C
Junction Temperature	T_j	150	°C
Thermal Resistance Junction to Ambient air	R_{0JA}	375	°C/W
Operating& Storage Temperature range	T_{stg}	-55 to 150	°C

¹⁾ Valid provided that electrodes are kept at ambient temperature.

Electrical Rating¹⁾ @ $T_A = 25^\circ C$, unless otherwise specified

Parameter	Symbol	Value	Unit
Repetitive Peak Reverse Voltage	V_{RRM}	75	V
Average rectified current sin half wave rectification with resistive load	$I_{F(AV)}$	150	mA
Repetitive Peak Forward Current at $T_{amb}=25^\circ C$	I_{FRM}	300	mA
Non-Repetitive Surge Forward Current at $t<1s$ and $T_j=25^\circ C$	I_{FSM}	500	mA
at $t \leq 8.3ms$ and $T_j=25^\circ C$		1000	mA

¹⁾ Valid provided that electrodes are kept at ambient temperature.

Electrical Characteristics¹⁾ @ $T_A = 25^\circ C$, unless otherwise specified

Parameter	Symbol	MAX.	Unit
Forward Voltage at $I_F=10mA$	V_F	1.0	V
at $I_F=100mA$		1.25	V
Leakage Current at $V_R=20V$	I_R	0.025	µA
Leakage Current at $V_R=75V$		5	µA
Capacitance at $V_R=0V$, $f=1MHz$	C_{tot}	4	pF
Reverse Recovery Time at $I_F=I_R=10mA, R_L=100\Omega$	t_{rr}	4	nS

¹⁾ Valid provided that electrodes are kept at ambient temperature.

Typical Characteristics@ $T_A = 25^\circ C$, unless otherwise specified

Figure 1. Forward Characteristic

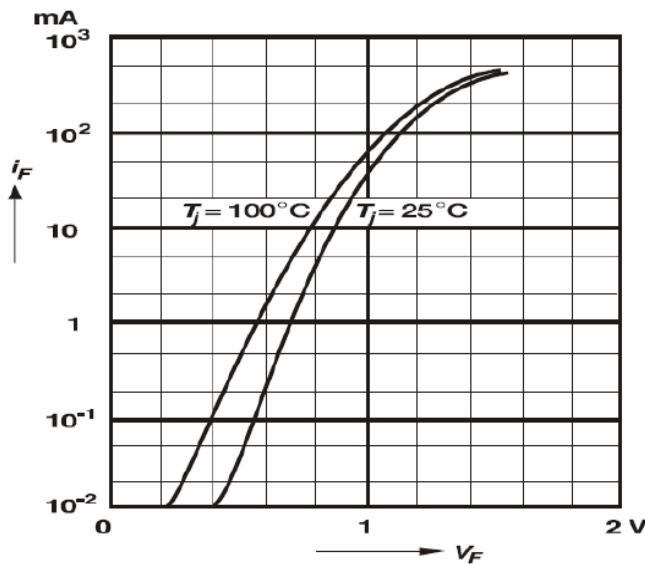


Figure 2. Power De-rating

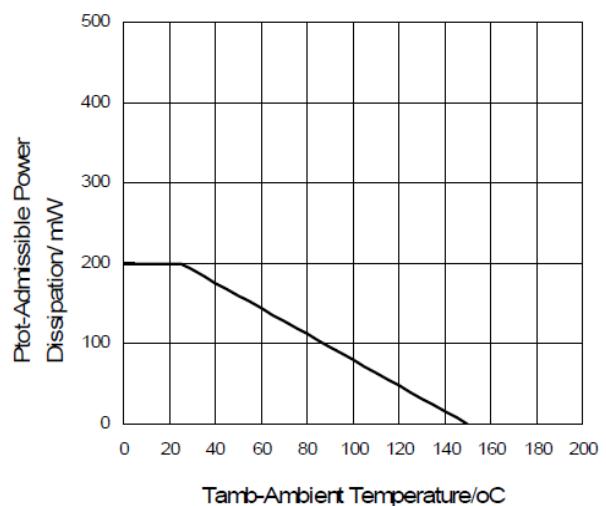


Figure 3. Forward Current De-rating

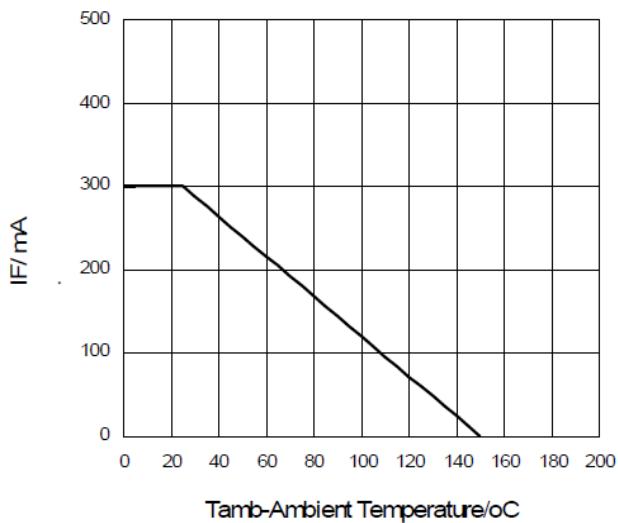


Figure 4. Reverse Voltage De-rating

