

# SS24FL THRU SS210FL



康比電子  
HORNBY ELECTRONIC

## SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

**REVERSE VOLTAGE:** 40 to 100 VOLTS

**FORWARD CURRENT:** 2.0 AMPERE

### FEATURES

- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Suffix "H" indicates Halogen-free parts, ex. SS24FLH

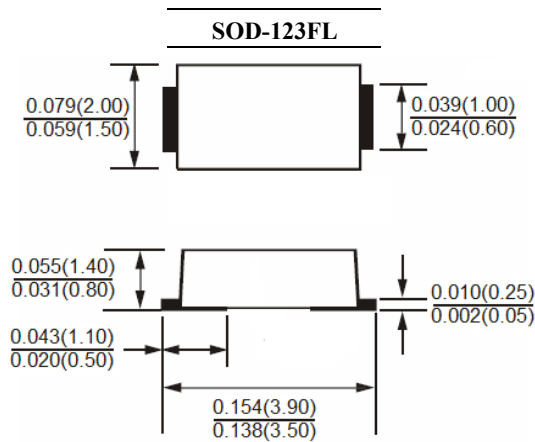
### MECHANICAL DATA

Case : Molded plastic, SOD-123FL

Terminals: Solder plated, solderable per

MIL-STD-750, method 2026 guaranteed

Polarity : Color band denotes cathode end



Dimensions in inches and (millimeters)

### Maximum Ratings and Electrical Characteristics

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Parameter	Symbols	SS24FL	SS26FL	SS210FL	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	40	60	100	Volts
Maximum RMS Voltage	$V_{RMS}$	28	42	70	Volts
Maximum DC Blocking Voltage	$V_R$	40	60	100	Volts
Maximum Average Forward Rectified Current (See Fig. 1)	$I_{(AV)}$	2.0			Amp
Peak Forward Surge Current, 8.3ms single half-sine-wave superimposed on rated load (JEDEC method)	$I_{FSM}$	50.0			Amp
Maximum Forward Voltage at 2.0A $T_A=25^\circ\text{C}$	$V_F$	0.55	0.75	0.85	Volts
Maximum Reverse Current at $T_A=25^\circ\text{C}$ at Rated DC Blocking Voltage $T_A=100^\circ\text{C}$	$I_R$	0.5 20			mAmp
Typical Thermal Resistance (Note 1)	$R_{\theta JA}$	85.0			°C/W
Operating Junction Temperature Range	$T_J$	-55 to +125	-55 to +150		°C
Storage Temperature Range	$T_{stg}$	-55 to +150			

#### NOTES:

1-  $f=1.0\text{MHz}$  and applied 4V DC reverse voltage

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### RATINGS AND CHARACTERISTIC CURVES

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

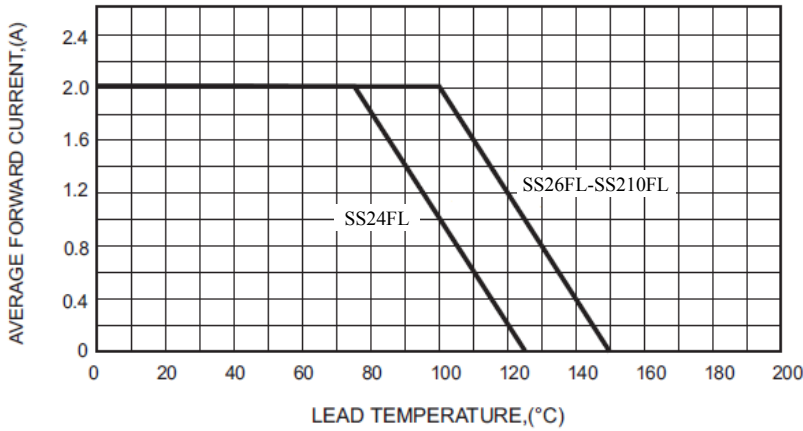


FIG.2-TYPICAL FORWARD CHARACTERISTICS

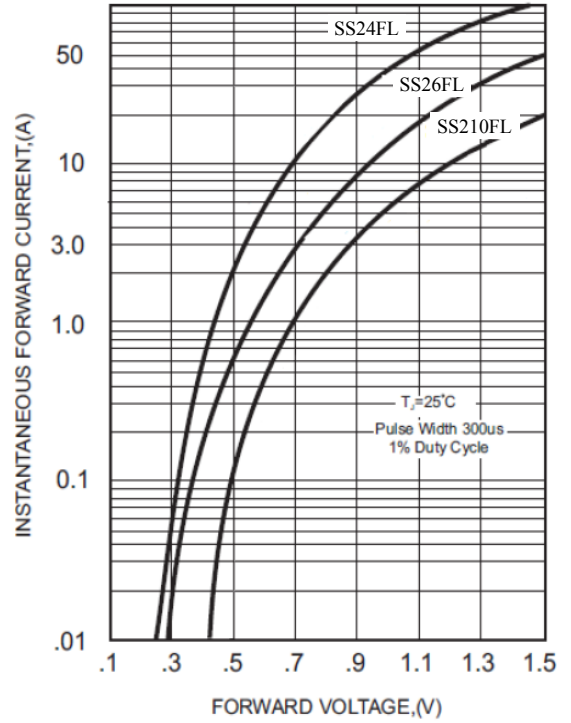


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

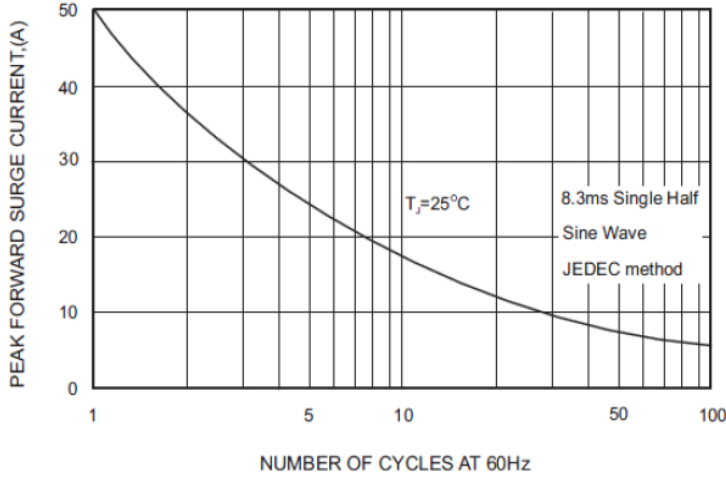


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

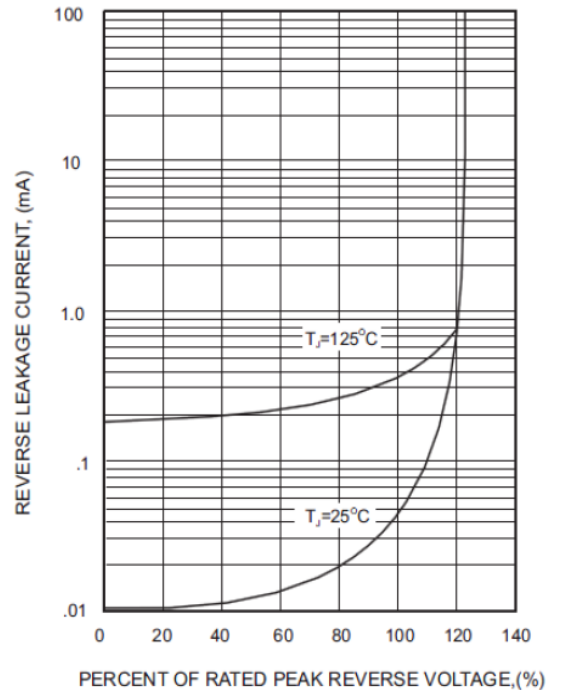


FIG.4-TYPICAL JUNCTION CAPACITANCE

